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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of:**

Kim C. Hardee

Serial No. 10/776,101

**Filed: February 11, 2004**

For: HIGH SPEED POWER-GATING TECHNIQUE  
FOR INTEGRATED CIRCUIT DEVICES  
INCORPORATING A SLEEP MODE OF OPERATION

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

Confirmation No.: Not Yet Assigned

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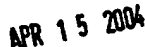
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